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Docket No.: 060188-0075

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
FUJIWARA, MAKOTO	:	Confirmation Number: 5700
Application No.: 09/867,766	:	Group Art Unit: 2134
Filed: May 31, 2001	:	Examiner: Poltorak, Piotr
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING SEMICONDUCTOR INTEGRATED CIRCUIT		

AMENDMENT

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated August 9, 2006, having a three-month shortened statutory period for response set to expire on November 9, 2006, reconsideration of the above-identified application is respectfully requested in view of the following amendment and remarks.

WDC99 1302790-1.060188.0075